PW120N10CS

Perfect MOS5 N-MOSFET 100V, 9.4mΩ, 60A

重庆平伟实业股份有限公司

Features

- Uses PingWei advanced PerfectMOS5 technology
- Extremely low on-resistance $R_{DS(on)}$
- Excellent $Q_g x R_{DS(on)}$ product(FOM)
- Excellent Low Ciss
- Qualified according to JEDEC criteria

Benefits

- High robustness and reliability
- Increases maximum current capability
- Low power loss, high power density
- Easy paralleling

Applications

- Synchronous Rectification for AC/DC Quick Charger
- Battery management
- UPS (Uninterrupible Power Supplies)

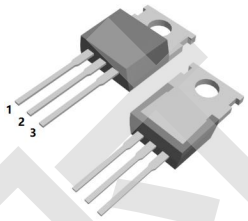
100% DVDS Tested

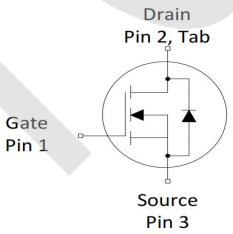
100% AvalancheTested

Product Summary

V_{DS}	100V
$R_{DS(on)@10V\ typ}$	9.4mΩ
I_D	60A

TO-220CB-3L





Package Marking and Ordering Information

Part #	Marking	Package	Packing	Reel Size	Tape Width	Qty
PW120N10CS	PW120N10CS	TO-220CB-3L	Tube	N/A	N/A	50pcs

Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	100	V
Continuous drain current T_C = 25°C (Silicon limit) T_C = 25°C (Package limit) T_C = 100°C (Silicon limit) Ta = 25°C	I_D	 60 80 38 9	A
Pulsed drain current (T_C = 25°C, t_p =100uS)	$I_{D\ pulse}$	240	A
Avalanche energy, single pulse (L=0.5mH，Vds=50V)	E_{AS}	36	mJ
Gate-Source voltage	V_{GS}	±20	V
Power dissipation T_C = 25°C Ta = 25°C	P_{tot}	 75 1.7	W
Operating junction and storage temperature	T_j , T_{stg}	-55...+150	°C
Soldering temperature, wave soldering only allowed at leads (1.6mm from case for 10s)	Tsold	260	°C

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Thermal Resistance

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Thermal resistance, junction – case.	RthJC	-	1.31	1.7	°C/W	-
Thermal resistance, junction - ambient(min. footprint)	RthJA	-	-	73	°C/W	-

Electrical Characteristic (at Tj = 25 °C, unless otherwise specified)

Parameter	**Symbol**	**Value**			**Unit**	**Test Condition**
		min.	**typ.**	**max.**		
Static Characteristic						
Drain-source breakdown voltage	BV_{DSS}	100	-	-	V	V_{GS}=0V, I_D=250uA
Gate threshold voltage	$V_{GS(th)}$	2	-	4	V	V_{DS}=V_{GS},I_D=250uA
Zero gate voltage drain current	I_{DSS}	- -	0.02 -	1 100	μA	V_{DS}=100V,V_{GS}=0V T_j=25°C T_j=150°C
Gate-source leakage current	I_{GSS}	-	±10	±100	nA	V_{GS}=±20V,V_{DS}=0V
Drain-source on-state resistance	$R_{DS(on)}$	-	9.4	12.0	mΩ	Vgs=10V,Id=20A
Transconductance	g_{fs}	-	26	-	S	V_{DS}=5V,I_D=20A
Dynamic Characteristic						
Input Capacitance	C_{iss}	-	1251	-	pF	V_{GS}=0V, V_{DS}=50V, f=1MHz
Output Capacitance	C_{oss}	-	434	-		
Reverse Transfer Capacitance	C_{rss}	-	30	-		
Gate Total Charge	Q_G	-	24	-	nC	Vds=80V,Id=20A ,Vgs=10V
Gate-Source charge	Q_{gs}	-	7	-		
Gate-Drain charge	Q_{gd}	-	9	-		
Turn-on delay time	$t_{d(on)}$	-	12	-	ns	V_{GS}=10V, V_{DD}=50V, R_{G_ext}=5Ω,I_D=20A
Rise time	t_r	-	38	-		
Turn-off delay time	$t_{d(off)}$	-	20	-		
Fall time	t_f	-	6	-		
Gate resistance	R_G	-	1.3	-	Ω	V_{GS}=0V, V_{DS}=0V, f=1MHz

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Body Diode Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Body Diode Forward Voltage	V_{SD}	-	0.88	1.2	V	V_{GS}=0V,I_{SD}=20A
Body Diode Continuous Forward Current	Is	-	-	60	A	TC = 25°C
Body Diode Pulsed Current	Is pulse	-	-	240	A	TC = 25°C
Body Diode Reverse Recovery Time	t_{rr}	-	56	-	ns	V_R=45V,I_F=5A, dI/dt=100A/μs
Body Diode Reverse Recovery Charge	Q_{rr}	-	139	-	nC	

通盛时代

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Typical Performance Characteristics

Fig 1: Output Characteristics

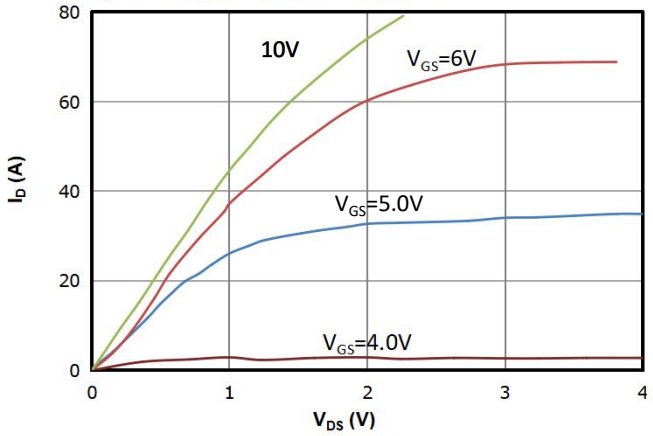

Fig 2: Transfer Characteristics

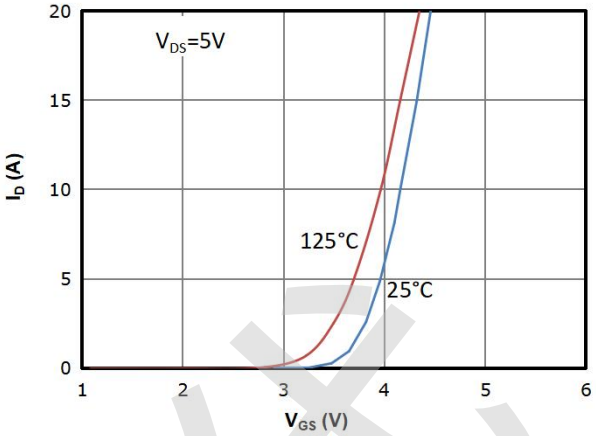

Fig 3: Rds(on) vs Drain Current and Gate Voltage

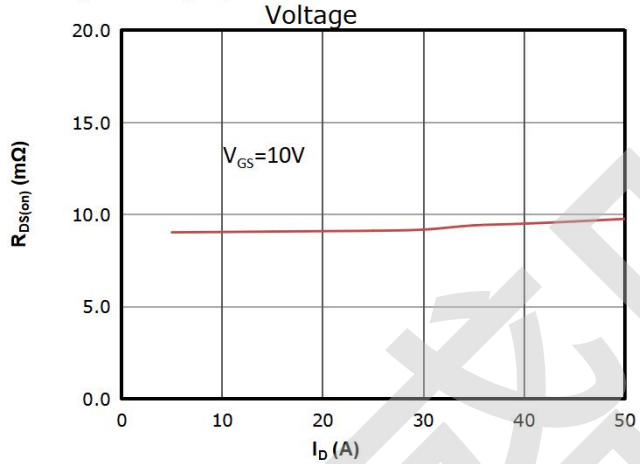

Fig 4: Rds(on) vs Gate Voltage

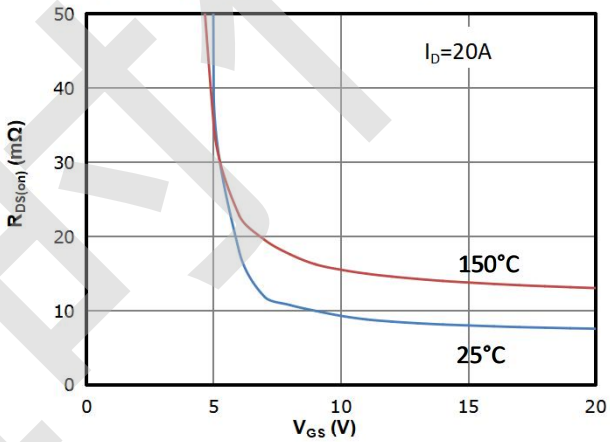

Fig 5: Rds(on) vs. Temperature

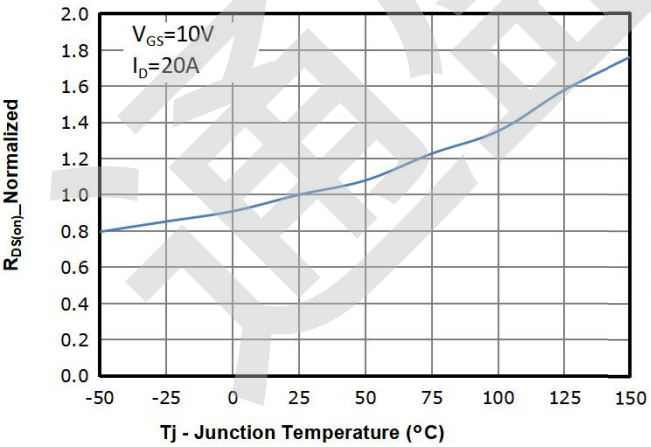

Fig 6: Vgs(th) vs. Temperature

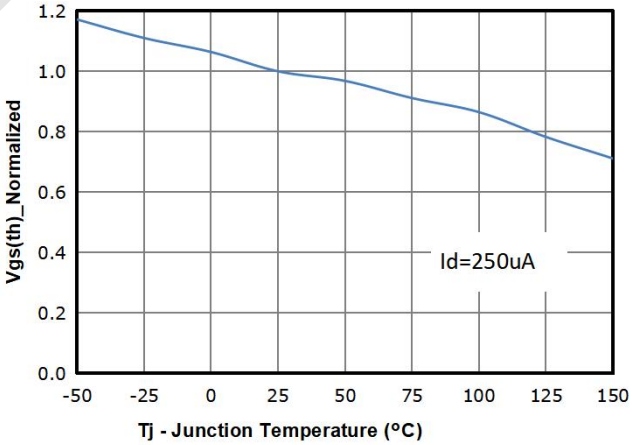

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Fig 7: BVdss vs. Temperature

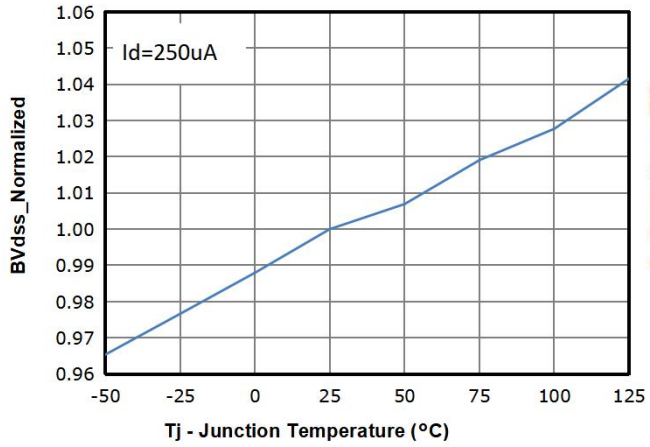

Fig 8: Capacitance Characteristics

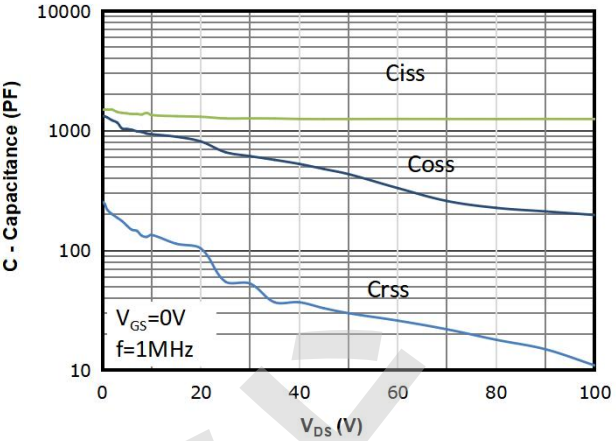

Fig 9: Gate ChargeCharacteristics

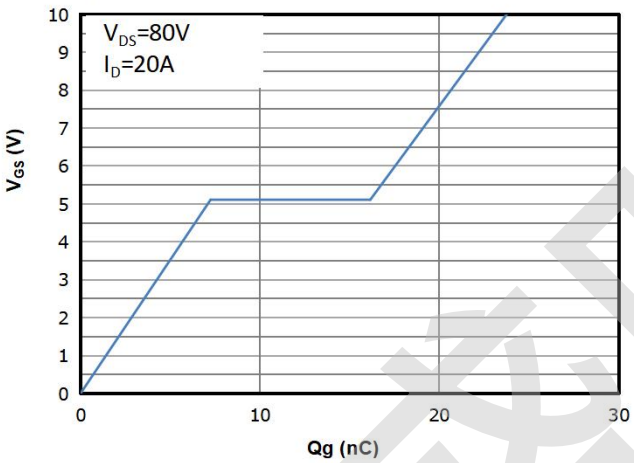

Fig 10: Body-diode Forward Characteristics

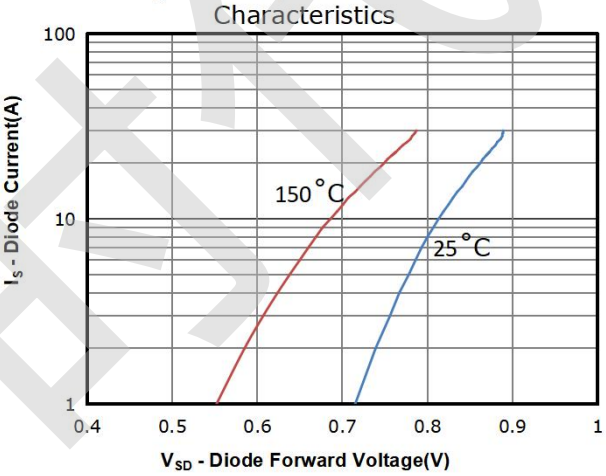

Fig 11: Power Dissipation

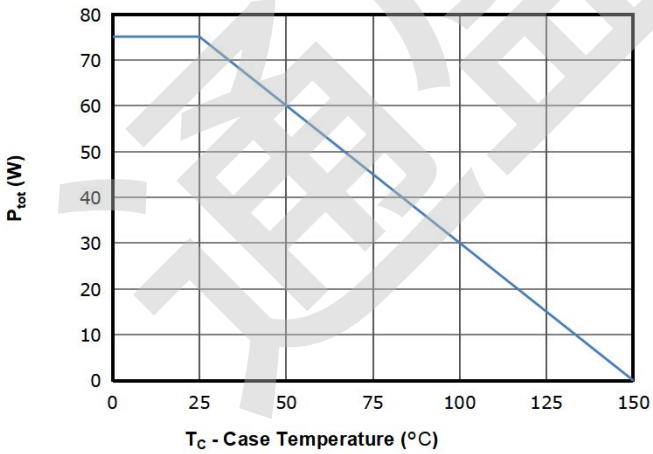

Fig 12: Drain Current Derating

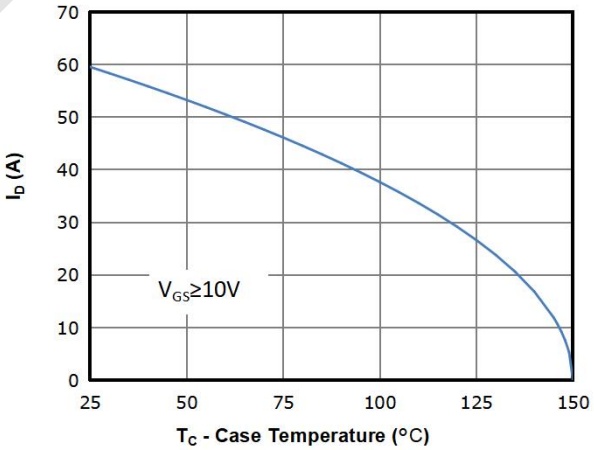

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Fig 13: Safe Operating Area

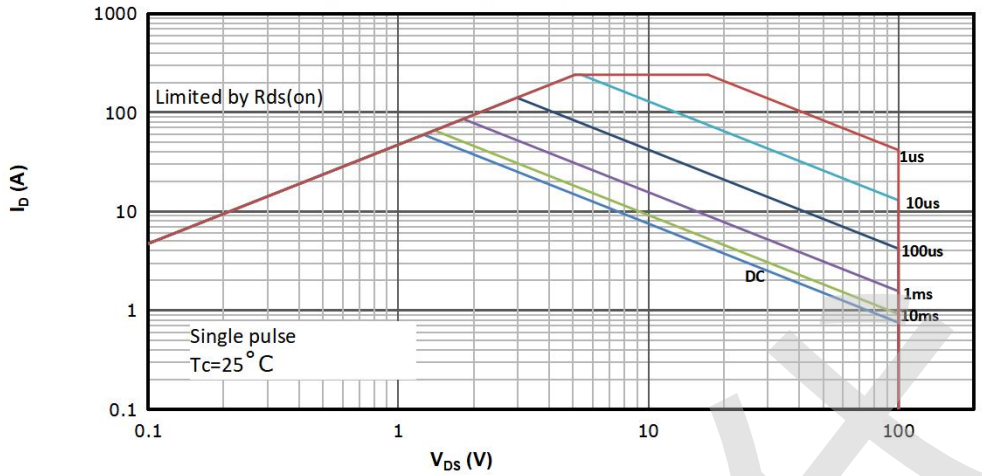
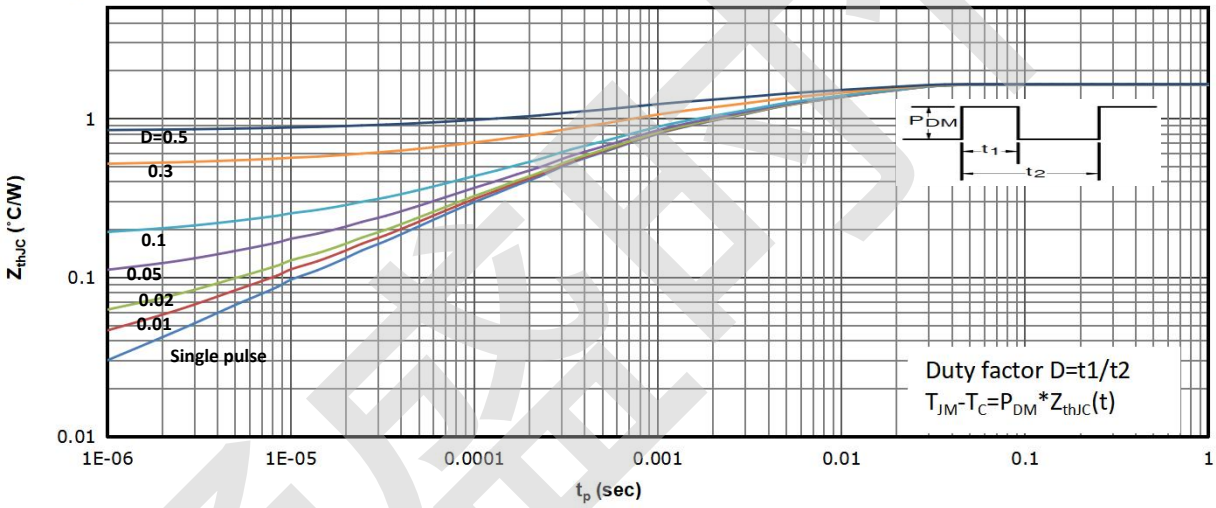


Fig 14: Max. Transient Thermal Impedance



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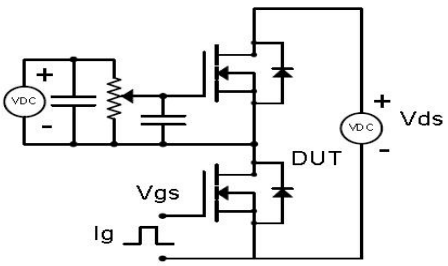
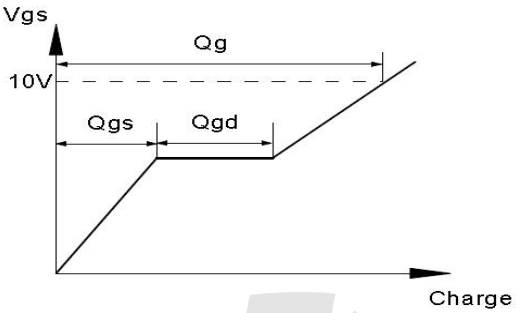
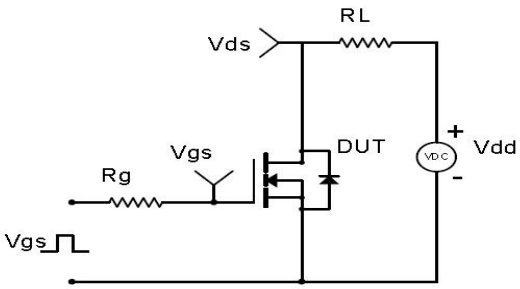
Test Circuit & Waveform

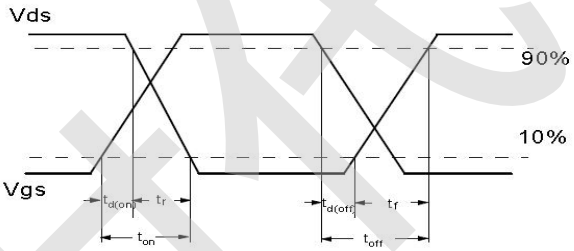
Gate Charge Test Circuit & Waveform



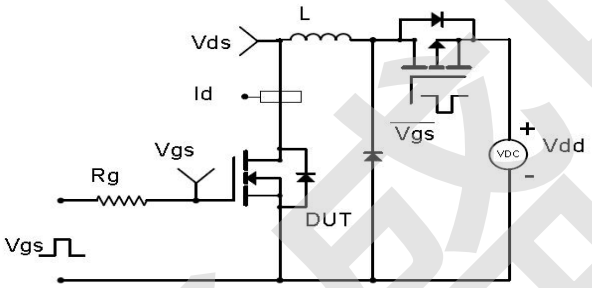


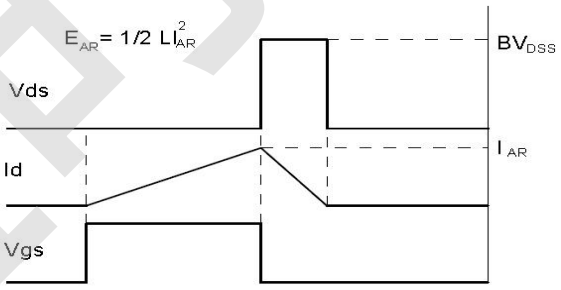
Resistive Switching Test Circuit & Waveforms



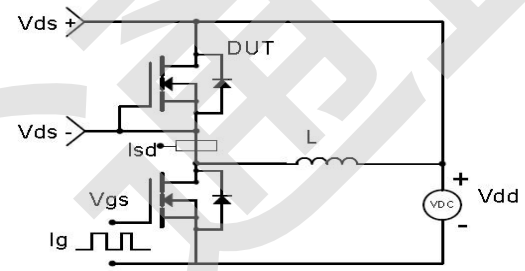


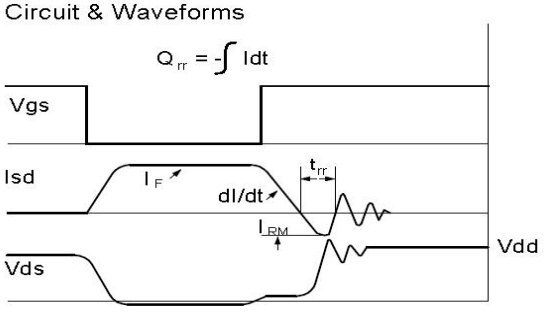
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms





Diode Recovery Test Circuit & Waveforms





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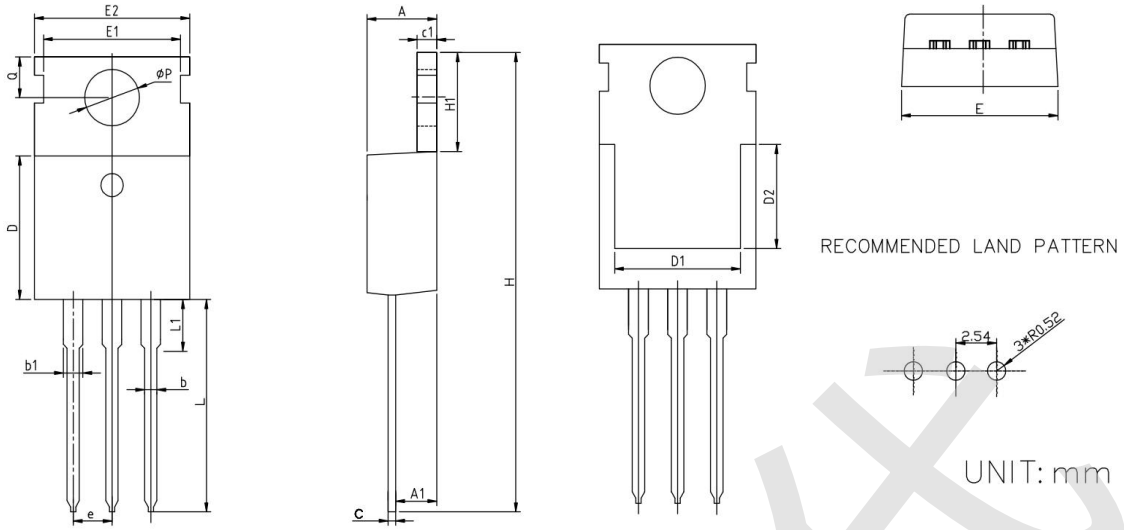
Package Outline: TO-220CB-3L



SYMBOL	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.40	4.80	0.173	0.189
A1	2.25	2.55	0.089	0.100
b	0.72	0.92	0.028	0.036
b1	1.12	1.42	0.044	0.056
c	0.40	0.60	0.016	0.024
c1	1.20	1.40	0.047	0.055
D	8.80	9.40	0.346	0.370
D1	7.75	8.15	0.305	0.321
D2	6.55	6.95	0.258	0.274
e	2.54		0.100	
E	9.65	10.35	0.380	0.407
E1	8.70		0.343	
E2	9.70	10.30	0.382	0.406
H	28.70	29.70	1.130	1.169
H1	6.25	6.85	0.246	0.270
L	13.20	13.80	0.520	0.543
L1	2.80	3.40	0.110	0.134
Q	2.60	3.00	0.102	0.118
φP	3.45	3.75	0.136	0.148

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Revision History

Revison	Date	Major changes
1.0	2023/3/29	Release of Formal Version.
1.1	2023/5/16	Update the Tdon/Tr/Tdoff/Tf parameter and Test Condition.

Disclaimer

Any and all semicondutor products have certain probability to fail or malfunction, which may result in personal injury, death or property damage. Customer are solely responsible for providing adequate safe measures when design their systems.

Unless otherwise specified in the datasheet, the product is designed and qulified as a standard commercial product and is not intended for use in applications that require extraordinary levels of quality and reliability, such as automotive, aviation/aerospace and life-support devices or systems.

Buyer is responsible for its products and applications using PingWei products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by PingWei.

"Typical" parameters which may be provided in PingWei data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts

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